

**PCN #242**  
**Notification Date:**  
**October 4, 2022**

**Product / Process Change Notice**

**Devices affected:**

The CEN1151A Schottky bridge rectifiers manufactured in the HDDIP case.

**Extent of change:**

Additional assembly site, internal die size optimization, and internal lead frame modification.

**Reason for change:**

As part of Central Semiconductor's supply chain risk mitigation initiative, in an effort to ensure an uninterrupted supply of product, an additional assembly site is being added for the referenced device. Product specifications, quality and reliability are not impacted by this addition.

**Effect of change:**

This addition does not affect the fit, form or function of devices.

**Qualification data:**

Test	Condition	Duration	Failure rate
Temperature Cycling (T.C.)	TA= -55+0°C/-10°C 10min(Min) TA= +150+15°C/-0°C 10min(Min) Transfer time less than 1min. The load should reach temp. within 15min	1000 Cycles	0/77
High Temp. Storage Life (H.T.S.L.)	TA= +150°C	1000 Hours	0/77
Resistance to Solder Heat (R.S.H.)	T =260°C ±5°C Dwell time = 10 sec.	1 Cycle	0/5
High Temp. Reverse Bias (H.T.R.B.)	TA=+100°C, VR=80%VB DC Supply	1000 Hours	0/77
Thermal Shock Test (T.S.T.)	TA=-65°C (5 min) ~ +150°C (5 min)	100 Cycles	0/77
Highly Accelerated Temperature and Humidity Stress Test (HAST)	TA=+85°C, RH=85%	1000 Hours	0/77
Solderability (S.D.)	Temperature of Solder Pot=245°C ±5°C Dip Time in Pot 5 sec ±0.5 sec	1 Cycle	0/15



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**Earliest effective date of change:**

October 4, 2022

**Sample availability:**

Please contact your salesperson or manufacturer's representative for samples.

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	