

## Product / Process Change Notice

**Part Affected:**

The CMPT5179, CMPT918 and CEN1056A RF transistors manufactured in the SOT-23 case.

**Extent of Change:**

Additional assembly location.

**Reason for Change:**

As part of Central Semiconductor’s supply chain risk mitigation initiative, and in an effort to ensure an undisrupted supply of product, an additional assembly location is being added for the referenced product. Device specifications, quality, and reliability are not impacted by this addition.

**Effect of Change:**

This addition does not affect the fit, form or function of the device.

**Qualification:**

No.	Test	Conditions (Reference standards are in bold)	Qty	Pass/Fail	Test Results
<b>1</b>	<b>Device Life Tests</b>				
A	High Temperature Reverse Bias (HTRB)	T=125°C, t = 1000 hours Bias Conditions per Device Datasheet <b>JESD22-A108</b>	77	Pass	77/77
B	High Temperature Storage Life (HTSL)	T=150°C, t = 1000 hours <b>JESD22-A103</b>	77	Pass	77/77
C	Thermal Shock	100 cycles, dwell time = 5 min, -65°C to +150°C, max transfer time = 20s <b>JESD22-A106</b>	77	Pass	77/77
D	Temperature Cycling (TC)	-65°C to +150°C, T <sub>dwell</sub> = 15min, 1000 cycles <b>MIL-STD-750 TM1051</b>	77	Pass	77/77
E	Resistance To Solder Shock	Pb free: T = 270°C ±5°C, Dwell=7s +2/-0 Thru-hole devices submerge to case, <b>JESD22-B106</b>	5	Pass	5/5
F	Solderability	Steam Age: T=93°C +3/-5°C. Pb-free Dip: T=245°C +/-5°C, Dwell time = 5+/-0.5sec <b>MIL-STD-750 TM2026</b>	15	Pass	15/15
G	Temperature and Humidity Storage (THS)	T = 85°C, RH = 85%, t = 1000 hrs as per <b>JESD22-A101</b> .	77	Pass	77/77



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<https://www.centra-semi.com/process-change-notices>

**PCN # 240**  
**Notification Date:**  
**September 8, 2022**

**Effective Date of Change:**

September 8, 2022

**Sample Availability:**

Please contact your salesperson or manufacturer's representative for samples.

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	