

**PCN #222**  
**Notification Date:**  
**June 14, 2021**

## Product / Process Change Notice

**Parts Affected:**

2N3442 NPN power transistor

**Extent of Change:**

Change in wafer fab, resulting in change in die size from 120 x 145 mils to 130 x 130 mils.

**Reason for Change:**

As part of Central Semiconductor's supply chain risk mitigation initiative, and in an effort to ensure an undisrupted supply of product, a change in wafer fabrication site is being made for the product referenced above. Product specifications, quality and reliability are not impacted by this change.

**Effect of Change:**

This change does not affect electrical device characteristics.

**Earliest Effective Date of Change:**

August 26, 2021

**Inventory Availability**

Existing inventory will be shipped until depleted.

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**Qualification:**

Test	Condition	Duration	Failure rate
High Temperature Storage Life	Ta=150°C	1000 Hours	0/77
Thermal Shock	dwel time = 5 min, -65°C to +150°C, max transfer time = 20 sec.	100 Cycles	0/77
High Temperature Reverse Bias	Ta=125°C, Bias conditions per device datasheet.	1000 Hours	0/77
Solderability	Steam Age: T=93°C +3/-5°C. Non Pb-free Dip: T=215°C +/-5°C, Pb-free Dip: T=245°C +/-5°C, Dwell time = 5+/-0.5sec	1 Cycle	0/15
Temperature Cycling	Dwell time = 15 min Ta = -65°C to +150°C Transfer time less than 1min.	1000 Cycles	0/77

**Sample Availability:**

Please contact your salesperson or manufacturer's representative for samples.

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	