**Product / Process Change Notice**

**Parts Affected:**

Low voltage transient voltage suppressors (TVS) manufactured in the SOD-123F case.

**Extent of Change:**

Additional wafer fab site.

**Reason for Change:**

As part of Central Semiconductor’s supply chain risk mitigation initiative, in an effort to ensure an undisrupted supply of product, an additional wafer fabrication site is being added for the referenced product family. Product specifications, quality and reliability are not impacted by this addition.

**Effect of Change:**

This addition does not affect the fit, form or function of the devices.

**Qualification:**

|  |  |  |  |
| --- | --- | --- | --- |
| **Test** | **Condition** | **Duration** | **Failure rate** |
| **Temperature Cycling (T.C.)** | TA= -55+0℃/-10℃ 10min(Min)  TA= +150+15℃/-0℃ 10min(Min)  Transfer time less than 1min.  The load should reach temp. within 15min | 1000 Cycles | 0/77  2 Lots |
| **High Temp. Storage Life (H.T.S.L.)** | TA= +150℃ | 1000 Hours | 0/77  2 Lots |
| **Resistance to Solder Heat (R.S.H.)** | T =260°C ±5°C Dwell time = 10 sec. | 1 Cycle | 0/30  2 Lots |
| **High Temp. Reverse Bias (H.T.R.B.)** | TA=+140℃, VR=80%VB  DC Supply | 1000 Hours | 0/77  2 Lots |
| **Thermal Shock Test (T.S.T.)** | TA=0℃ (5 min) ~ +100℃ (5 min) | 100 Cycles | 0/77  2 Lots |

**Effective Date of Change:**

Existing inventory will be shipped until depleted.

**Sample Availability:**

Please contact your salesperson or manufacturer’s representative for samples.

**Part Numbers Affected:**

CMF5.0A

CMF6.0A

CMF6.5A

CMF7.0A

CMF7.5A

CMF8.0A

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor’s Product/Process Change Notification (PCN).

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| Company Name: |  |
| Address: |  |
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| Printed Name: |  |
| Title: |  |
| Signature: |  |
| Date: |  |