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<https://www.centrasemi.com/process-change-notices>

**PCN #203**

**Initial Notification Date: August 5, 2021**

**Final Notification Date: November 10, 2021**

**Product / Process Change Notice**

**Parts Affected:**

All devices manufactured in the SOIC-8 package.

**Extent of Change:**

The composition of the bond wire is changing from Gold (**Au**) to Copper/Palladium/Gold (**CuPdAu**).

**Reason for Change:**

This change will insure an uninterrupted flow of product and provide increased flexibility for supply chain management.

**Effect of Change:**

This change does not affect the form, fit, or function of any device.

**Qualification Data:**

Test	Condition	Duration	Failure rate
<b>Preconditioning</b>	Sequence: initial electrical test, visual inspection, temperature cycle, 24hr bake, MSL1 moisture soak, reflow, flux soak, clean & dry, and final electrical test. <b>JESD22-A113</b>	-	0/325
<b>Resistance to Solder Heat (R.S.H.)</b>	T =260°C ±5°C Dwell time = 10 sec. <b>JESD22-B106</b>	1 Cycle	0/5
<b>Temperature Cycling (T.C.)</b>	1000 cycles. Dwell time = 15 min Ta = -65°C to +150°C <b>JESD22-A104</b>	1000 Cycles	0/77
<b>Thermal Shock Test (T.S.T.)</b>	TA=-65°C (5 min) to +150°C (5 min) <b>JESD22-A106</b>	100 Cycles	0/77

**Earliest Effective Date of Change:**

February 2, 2022



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**Part Numbers Affected:**

- CWDM3011N
- CWDM3011P
- CWDM305N
- CWDM305ND
- CWDM305P
- CWDM305PD

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	