**Product / Process Change Notice**

**Parts Affected:**

Silicon controlled rectifiers (SCRs) manufactured in the TO-39 Case.

**Extent of Change:**

Additional assembly site.

**Reason for Change:**

As part of Central Semiconductor’s supply chain risk mitigation initiative, in an effort to ensure an undisrupted supply of product, an additional assembly site is being added for the referenced product family. Product specifications, quality and reliability are not impacted by this addition.

**Effect of Change:**

This addition does not affect the fit, form or function of the devices.

**Qualification:**

|  |  |  |  |
| --- | --- | --- | --- |
| **Test** | **Condition** | **Duration** | **Failure rate** |
| **Temperature Cycling (T.C.)** | TA= -55°C to +175°C  Dwell time = 15 min.  **JESD22-A104 &** **MIL-STD-750 TM1051** | 1000 Cycles | 0/77 |
| **High Temp. Storage Life (H.T.S.L.)** | TA= +150℃ (-0/+10°C)  **JESD22-A103** | 1000 Hours | 0/77 |
| **High Temp. Reverse Bias (H.T.R.B.)** | T=+125℃  Bias conditions per Data Sheet  **JESD22-A108** | 1000 Hours | 0/77 |
| **Resistance to Solder Heat (R.S.H.)** | Pb free: T =270°C ±5°C, Dwell=7s +2/-0  Thru-hole devices submerge to case  **JESD22-B106** | 1 Cycle | 0/5 |
| **Thermal Shock Test (T.S.T.)** | T=-65℃ to +150°C  Dwell time = 5 min.  Max Transfer time 20 sec.  **JESD22-A106** | 100 Cycles | 0/77 |
| **Solderability (SD)** | Steam Age: T=93°C +3/-5°C.  Pb-free Dip: T=245°C +/-5°C  Dwell time = 5+/-0.5sec  **MIL-STD-750 TM2026** | 1 Cycle | 0/15 |

**Effective Date of Change:**

Existing inventory will be shipped until depleted.

**Sample Availability:**

Please contact your salesperson or manufacturer’s representative for samples.

**Part Numbers Affected**

|  |  |  |
| --- | --- | --- |
| CS39-2A | 2N1595 | 2N2322 |
| CS39-2B | 2N1595A | 2N2323 |
| CS39-2D | 2N1596 | 2N2324 |
| CS39-4B | 2N1596A | 2N2325 |
| CS39-4D | 2N1597 | 2N2326 |
| CS39-4M | 2N1597A | 2N2326A |
| CS39-4N | 2N1598 | 2N2327 |
|  | 2N1598A | 2N2328 |
|  | 2N1599 | 2N2329 |
|  | 2N1599A |  |

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor’s Product/Process Change Notification (PCN).

|  |  |
| --- | --- |
| Company Name: |  |
| Address: |  |
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|  |
| Printed Name: |  |
| Title: |  |
| Signature: |  |
| Date: |  |