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<https://www.centrasemi.com/process-change-notices>

**PCN # 188**  
**Notification Date:**  
**June 15, 2020**

## Product / Process Change Notice

**Parts Affected:**

5A ultrafast recovery rectifiers manufactured in the SMC case:  
 CMR5U-02, CMR5U-04, CMR5U-06, CMR5U-08.

**Extent of Change:**

Additional wafer fab site.

**Reason for Change:**

As part of Central Semiconductor's supply chain risk mitigation initiative, in an effort to ensure an undisrupted supply of product, an additional wafer fabrication site is being added for the referenced product families. Product specifications, quality and reliability are not impacted by this addition.

**Effect of Change:**

This addition does not affect the fit, form or function of the devices.

**Qualification:**

Test	Condition	Duration	Failure rate
<b>Temperature Cycling (T.C.)</b>	TA= -55+0°C/-10°C 10min(Min) TA= +150+15°C/-0°C 10min(Min) Transfer time less than 1min. The load should reach temp. within 15min	1000 Cycles	0/77 3 Lots
<b>High Temp. Storage Life (H.T.S.L.)</b>	TA= +150°C	1000 Hours	0/77 3 Lots
<b>Continue Forward Operating Life (C.F.O.L.)</b>	TA=+25°C, VR=100%VB I=IO±10% DC supply	168 Hours	0/77 3 Lots
<b>Resistance to Solder Heat (R.S.H.)</b>	T =260°C ±5°C Dwell time = 10 sec.	1 Cycle	0/30 3 Lots
<b>Intermittent Forward Operation Life (I.O.L.)</b>	I=1.0 × IF DC supply On time: 2mins at least, Off time: 2mins at least	15,000 Cycles	0/77 3 Lots
<b>Autoclave (A.C.)</b>	Temperature = 121°C ± 2°C; relative humidity = 100%; vapor pressure = 29.7 psia (15psig)	96 Hours	0/77 3 Lots
<b>Thermal Shock Test (T.S.T.)</b>	TA=0°C (5 min) ~ +100°C (5 min)	100 Cycles	0/77 3 Lots



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Test	Condition	Duration	Failure rate
<b>Solderability (SD)</b>	Temperature of solder Pb free: POT=245±5°C Solder immersion time=5±0.5 sec Dipping depth= within 1.27mm of the body.	1 Cycle	0/10 3 Lots
<b>Temperature Humidity Storage (T.H.S.)</b>	TA=+85°C RH=85%	168 Hours	0/30 3 Lots
<b>Forward Surge Current (F.S.C.)</b>	Square wave or Sine wave; IFSM=Data Sheet Spec Duration of Pulse (tp)=8.3ms	1 Cycle	0/22 3 Lots

**Effective Date of Change:**

Existing inventory will be shipped until depleted.

**Sample Availability:**

Please contact your salesperson or manufacturer's representative for samples.

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	