**Product / Process Change Notice**

**Parts Affected:**

5A ultrafast recovery rectifiers manufactured in the SMC case:

CMR5U-02, CMR5U-04, CMR5U-06, CMR5U-08.

**Extent of Change:**

Additional wafer fab site.

**Reason for Change:**

As part of Central Semiconductor’s supply chain risk mitigation initiative, in an effort to ensure an undisrupted supply of product, an additional wafer fabrication site is being added for the referenced product families. Product specifications, quality and reliability are not impacted by this addition.

**Effect of Change:**

This addition does not affect the fit, form or function of the devices.

**Qualification:**

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| **Test** | **Condition** | **Duration** | **Failure rate** |
| **Temperature Cycling (T.C.)** | TA= -55+0℃/-10℃ 10min(Min) TA= +150+15℃/-0℃ 10min(Min) Transfer time less than 1min. The load should reach temp. within 15min  | 1000 Cycles | 0/773 Lots  |
| **High Temp. Storage Life (H.T.S.L.)** | TA= +150℃ | 1000 Hours | 0/773 Lots |
| **Continue Forward Operating Life (C.F.O.L)** | TA=+25℃, VR=100%VB I=IO±10% DC supply  | 168 Hours | 0/773 Lots  |
| **Resistance to Solder Heat (R.S.H.)** | T =260°C ±5°CDwell time = 10 sec. | 1 Cycle | 0/303 Lots  |
| **Intermittent****Forward****Operation Life (I.O.L.)** | I=1.0 × IF DC supply On time: 2mins at least, Off time: 2mins at least  | 15,000 Cycles | 0/773 Lots  |
| **Autoclave (A.C.)** | Temperature = 121°C ± 2°C; relative humidity = 100%; vapor pressure = 29.7 psia (15psig) | 96 Hours | 0/773 Lots  |
| **Thermal Shock Test (T.S.T.)** | TA=0℃ (5 min) ~ +100℃ (5 min)  | 100 Cycles | 0/773 Lots  |

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| **Test** | **Condition** | **Duration** | **Failure rate** |
| **Solderability (SD)**  | Temperature of solder Pb free: POT=245±5℃ Solder immersion time=5±0.5 sec Dipping depth= within 1.27mm of the body.  | 1 Cycle | 0/103 Lots  |
| **Temperature Humidity Storage (T.H.S.)** | TA=+85℃ RH=85%  | 168 Hours | 0/303 Lots |
| **Forward Surge Current (F.S.C.)** | Square wave or Sine wave; IFSM=Data Sheet Spec Duration of Pulse (tp)=8.3ms | 1 Cycle | 0/223 Lots  |

**Effective Date of Change:**

Existing inventory will be shipped until depleted.

**Sample Availability:**

Please contact your salesperson or manufacturer’s representative for samples.

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor’s Product/Process Change Notification (PCN).

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| Company Name: |  |
| Address: |  |
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| Printed Name: |  |
| Title: |  |
| Signature: |  |
| Date: |  |