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https://www.centralsemi.com/process-change-notices

PCN #178
Notification Date:
January 2, 2020

# **Product / Process Change Notice**

#### **Parts Affected:**

General purpose and ultrafast recovery rectifiers manufactured in the SMA, SMAFL, SMB, SMC, and DO-201 cases.

#### **Extent of Change:**

Addition of new wafer fab.

# **Reason for Change:**

As part of Central Semiconductor's supply chain risk mitigation initiative, and in an effort to ensure undisrupted product supply, an additional wafer fabrication site is being added for the referenced product families. Product specifications, quality and reliability are not impacted by this addition.

#### **Effect of Change:**

This addition does not affect the fit, form or function of the devices.

#### **Qualification:**

Test	Condition	Duration	Failure rate
Pre Conditioning(P.C) (SMD qualification parts before test T.C., A.C, T.H.B, I.O.L and R.S.H)	1. TCT -55~+150 °C ,5 cycle 2. Bake 125 +5/-0 °C , 24hours 3. Temperature humidity 85°C/85%RH, 168hours 4. Reflow 3 times	1 Cycle	
Temperature Cycling (T.C.)	TA= -55+0°C/-10°C 10min(Min) TA= +150+15°C/-0°C 10min(Min) Transfer time less than 1min. The load should reach temp. within 15min	1000 Cycles	
High Temperature Reverse Bias (HTRB)	TA=140°C, VR=100%VB DC supply	1000 Hours	0/77 3 Lots
Resistance to Solder Heat (R.S.H.)	T =260°C ±5°C  Dwell time = 10 sec.  1 Cycle		0/30 3 Lots
Intermittent Forward Operation Life (I.O.L.)	I=1.0 × IF DC supply On time: 2mins at least, Off time: 2mins at least		0/77 3 Lots
Autoclave (A.C.)	toclave (A.C.)  Temperature = 121°C ± 2°C; relative humidity = 100%; vapor pressure = 29.7 psia (15psig)  96		0/77 3 Lots
High Humidity High Temperature Reverse Bias (H3TRB)	cure Reverse VR=80%VB(customer spec.)		0/77 3 Lots

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Test	Condition	Duration	Failure rate
Solderability (SD)	Temperature of solder Pb free: POT=245±5°C  Solder immersion time=5±0.5 sec  Dipping depth= within 1.27mm of the body.		0/10 3 Lots
ESD - HBM	100 pF / 1.5 KΩ	3 Cycles	0/30 3 Lots
ESD - MM	200pF / 0Ω	3 Cycles	0/30 3 Lots

# **Effective Date of Change:**

Existing inventory will be shipped until depleted.

## Sample Availability:

Please contact your salesperson or manufacturer's representative for samples.

## **Part Numbers Affected:**

SMA Case	SMAFL Case	SMB Case	SMC Case	DO-201 Case
CMR1U-01M	CMR2-04MFL	CMR1U-01	CMR3-02	CR3U-010
CMR1U-02M	CMR2-06MFL	CMR1U-02	CMR3-04	CR3U-020
CMR1U-04M	CMR2-08MFL	CMR1U-04	CMR3-06	CR3U-040
CMR1U-06M	CMR2-10MFL	CMR1U-06	CMR3-08	CR3U-060
CMR1U-10M		CMR1U-08	CMR3-10	CR3U-080
CMR1U-13M		CMR1U-10	CMR3-11	
		CMR2U-01	CMR3U-01	
		CMR2U-02	CMR3U-02	
		CMR2U-04	CMR3U-04	
		CMR2U-06	CMR3U-06	
		CMR2U-08	CMR3U-10	
		CMR2U-10		
		CMR3U-01M		
		CMR3U-02M		
		CMR3U-04M		
		CMR3U-06M		
		CMR3U-10M		

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As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	

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