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PCN #193
Notification Date:
July 29, 2020

Product / Process Change Notice

Parts Affected:

Fast, hyperfast, superfast and ultrafast recovery rectifiers manufactured in DO-41, DPAK, D2PAK, SMA, SMAFL, SMB, SMC and SOD-123FL cases.

Extent of Change:

Additional wafer fab site.

Reason for Change:

As part of Central Semiconductor’s supply chain risk mitigation initiative, in an effort to ensure an undisrupted supply of product, an additional wafer fabrication site is being added for the referenced product families. Product specifications, quality and reliability are not impacted by this addition.

Effect of Change:

This addition does not affect the fit, form or function of the devices.

Qualification:

Test	Condition	Duration	Failure rate
Temperature Cycling (T.C.)	TA= -55+0°C/-10°C 10min(Min) TA= +150+15°C/-0°C 10min(Min) Transfer time less than 1min. The load should reach temp. within 15min	1000 Cycles	0/77 3 Lots
High Temp. Storage Life (H.T.S.L.)	TA= +150°C	1000 Hours	0/77 3 Lots
Continue Forward Operating Life (C.F.O.L.)	TA=+25°C, VR=100%VB I=IO±10% DC supply	168 Hours	0/77 3 Lots
Resistance to Solder Heat (R.S.H.)	T =260°C ±5°C Dwell time = 10 sec.	1 Cycle	0/30 3 Lots
Intermittent Forward Operation Life (I.O.L.)	I=1.0 × IF DC supply On time: 2mins at least, Off time: 2mins at least	15,000 Cycles	0/77 3 Lots
High Temp. Reverse Bias (H.T.R.B.)	TA=+140°C, VR=80%VB DC Supply	96 Hours	0/77 3 Lots
Thermal Shock Test (T.S.T.)	TA=0°C (5 min) ~ +100°C (5 min)	100 Cycles	0/77 3 Lots



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Effective Date of Change:

Existing inventory will be shipped until depleted.

Sample Availability:

Please contact your salesperson or manufacturer's representative for samples.

Part Numbers Affected:

DO-41 case	DPAK case	D2PAK case	SMA case	SMAFL case	SMB case	SMC case	SOD-123FL case
CR1S-040	CUD3-02	CUDD8-04	CEN1253	CMR1H-04MFL	CMR1S-01	CMR3S-01	CMMR1F-02
	CUD6-02C				CMR1S-02	CMR3S-02	CMMR1F-04
					CMR2S-02	CMR3S-04	CMMR1F-06
					CMR2S-04	CMR3S-06	CMMR1F-08
					CMR2S-06		CMMR1S-02
							CMMR1U-02
							CMMR1U-04
							CMMR1U-06
							CMMR1U-08

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	