

Diac

DO-35 Case

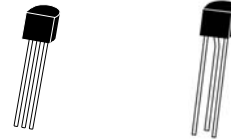


TYPE NO.	V_{BO} (V)		ΔV_{BO} (V) MAX	I_{BO} (μ A) MAX	$ \Delta V_{\pm} $ (V) MIN	I_P (A) MAX
	MIN	MAX				
CT-32	28	36	3.0	50	5.0	2.0

Silicon Bilateral Switch (SBS)

TO-92 Case

TYPE NO.	SWITCHING VOLTAGE		SWITCHING CURRENT I_S (μ A) MAX	HOLDING CURRENT I_H (μ A) MAX	OFF STATE BLOCKING CURRENT	
	V_S (V) MIN	MAX			I_B (μ A) MAX	V_F (V)
MPS4992	7.5	9.0	120	500	0.1	5.0



TO-92

TO-92-18R

Programmable UJTs (PUT)

TO-92 Case, TO-92-18R Case

TYPE NO.	GATE TO ANODE REVERSE VOLTAGE V_{GAR} (V) MIN	DC ANODE CURRENT I_T (mA) MIN	GATE TO ANODE LEAKAGE CURRENT I_{GAO} (nA) $V_S=40V_{dc}$ MAX	PEAK CURRENT		VALLEY CURRENT		CASE
				$R_G=10K\Omega$ (μ A) MAX	$R_G=1.0M\Omega$ (μ A) MAX	$R_G=10K\Omega$ (μ A) MIN	$R_G=1.0M\Omega$ (μ A) MAX	
2N6027	40	150	10	5.0	2.0	70	50	TO-92
2N6028	40	150	10	1.0	0.15	25	25	TO-92
PN6119-18R	40	300	10	5.0	2.0	70	50	TO-92-18R
PN6120-18R	40	300	10	1.0	0.15	25	25	TO-92-18R

Unijunction Transistors (UJTs)

TO-92 Case

TYPE	INTRINSIC STANDOFF RATIO η		INTERBASE RESISTANCE r_{BB}		PEAK-POINT CURRENT I_P MAX μ A	EMITTER REV. CURRENT I_{EB20} MAX μ A		VALLEY-POINT CURRENT I_V MIN mA
	MIN	MAX	MIN k Ω	MAX k Ω		$@V_{B2E}$ V		
2N4870	0.56	0.75	4.0	9.1	5.0	1.0	30	2.0
2N4871	0.70	0.85	4.0	9.1	5.0	1.0	30	4.0

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