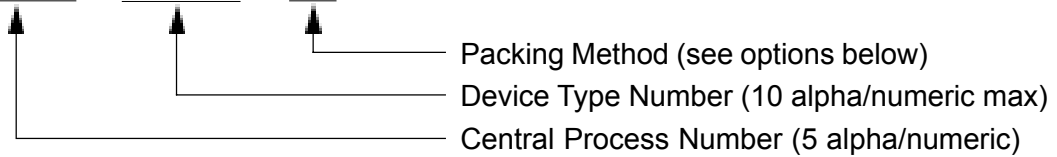


# Chip Part Numbering System

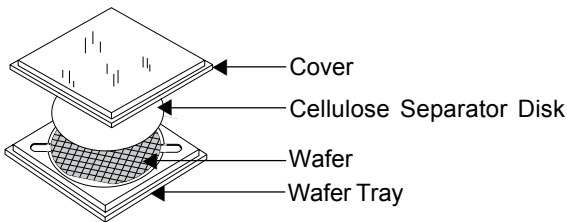
This specification defines the method for chip packing and part number identification

example: Small Signal NPN High Voltage Transistor Chip, 2N3439, Waffle Packed.

**CP301 - 2N3439 - CT**

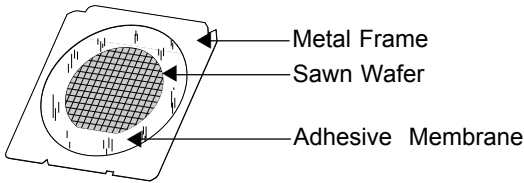


## Packing Method Options



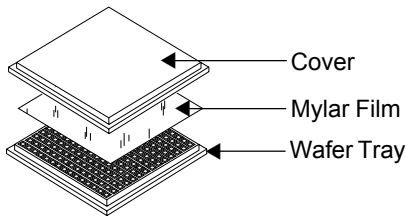
### Wafer Form

- ◆ 100% tested with rejects inked
- ◆ Use **-WN** suffix when ordering



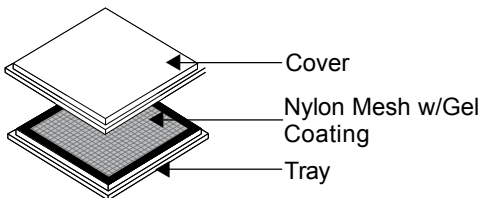
### Sawn Wafer

- ◆ 100% tested with rejects inked.
- ◆ Mounted on adhesive membrane on a metal frame .
- ◆ Use **-WS** suffix when ordering.



### Chip Form

- ◆ 100% tested with inked rejects removed.
- ◆ Waffle Packed.
- ◆ Use **-CT** suffix when ordering.



### GEL-PAK

- ◆ 100% tested with inked rejects removed.
- ◆ Resistance to chip movement compared to standard waffle pack.
- ◆ Use **-CG** suffix when ordering.