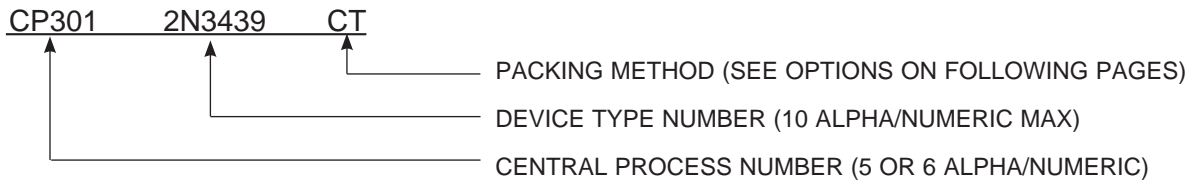


Chip Numbering System

Chip Part Numbering System

- ◆ This specification defines the method for chip packing and part number identification.
example: Small Signal NPN High Voltage Transistor Chip, 2N3439, Waffle Packed.



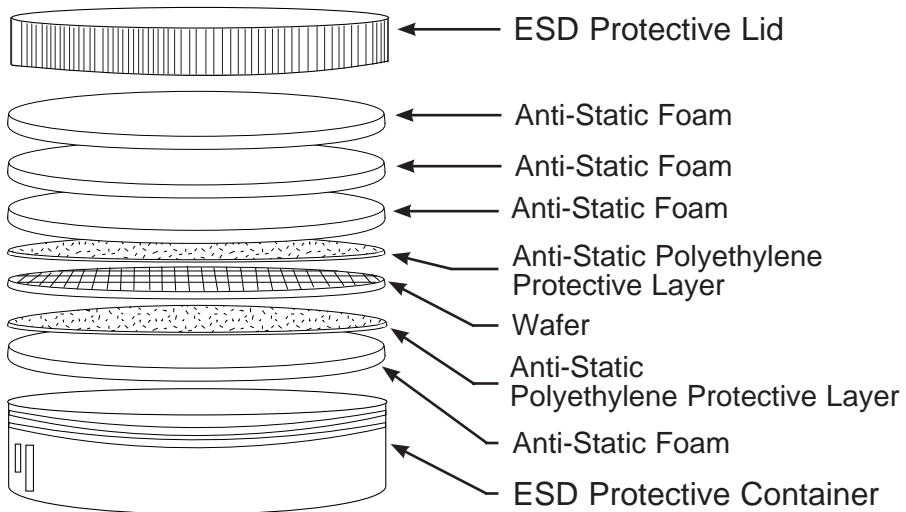
Packing Options

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Packing Options

WAFER FORM

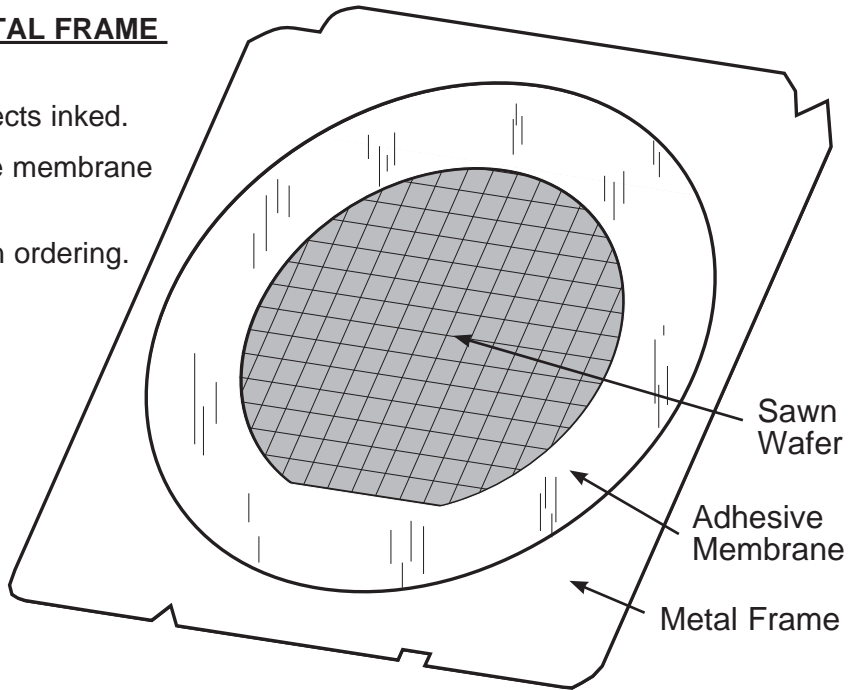
- ◆ 100% tested with rejects inked
- ◆ Use - **WN** suffix when ordering



Packing Options

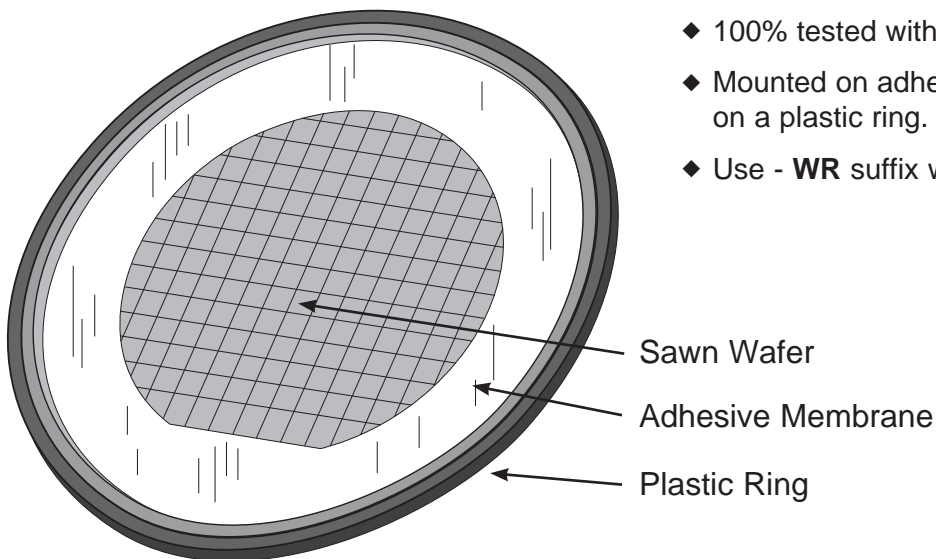
SAWN WAFER ON METAL FRAME

- ◆ 100% tested with rejects inked.
- ◆ Mounted on adhesive membrane on a metal frame .
- ◆ Use - **WS** suffix when ordering.



SAWN WAFER ON PLASTIC RING

- ◆ 100% tested with rejects inked.
- ◆ Mounted on adhesive membrane on a plastic ring.
- ◆ Use - **WR** suffix when ordering.



Packing Options

CHIP FORM

- ◆ Waffle Packed.
- ◆ Use: **-CT**, **-CM**, **-CTO** suffix when ordering.
 - ◆ **-CT** (100% tested with rejects removed).
 - ◆ **-CM** (100% tested and 100% visually inspected per MIL-STD-750, [method 2072 transistors] [method 2073 diodes] with rejects removed).
 - ◆ **-CTO** (100% tested with rejects removed, die orientation as specified by customer.)



OPTIONAL PACKING *Available on special order*

- ◆ **Gel-Pak** (www.gelpak.com)
- ◆ **Surftape®** Tape and Reel (www.surftape.net)

For additional information on these packing options, please consult factory.

QUALITY POLICY

- ◆ Our definition of quality is **Complete Customer Satisfaction**.
- ◆ We are dedicated to manufacturing Competitively Priced, **Quality Products** delivered on time and professionally serviced.
- ◆ We define **Excellence** as surpassing our customer's expectations.
- ◆ Our perpetual challenge is the pursuit of **Achieving Excellence** in everything we do, and we strive to accomplish this by utilizing Ongoing Training for Continuous Improvement in all areas.
- ◆ We recognize that customer satisfaction results in **Repeat Business**.